

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,424,397 B2
APPLICATION NO. : 10/635083
DATED : September 9, 2008
INVENTOR(S) : Richard W. Adkisson et al.

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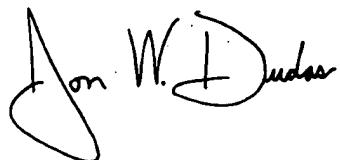
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, line 36, insert -- Related subject matter disclosed in the following commonly owned co-pending U. S. patent applications: (i) A BUS INTERFACE MODULE, filed March 28, 2003; Application No. 10/402,092; and (ii) AN INTEGRATED CIRCUIT, filed March 28, 2003; Application No. 10/402,034, is hereby incorporated by reference. -- above "BACKGROUND".

In column 7, line 59, in Claim 15, delete "(GFFC)" and insert -- ("GPPC") --, therefor.

Signed and Sealed this

Twenty-third Day of December, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office